

Refine Search

Your wildcard search against 10000 terms has yielded the results below.

Your result set for the last L# is incomplete.

The probable cause is use of unlimited truncation. Revise your search strategy to use limited truncation.

Search Results -

Terms	Documents
L11 and bit\$ and (error\$ or signal\$ or except\$)	35

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Search:	<input type="text" value="L12"/> [Print]	Refine Search Recall Text Clear Interrupt
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Search History

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Set

Name Query

side by
side

DB=USPT; PLUR=YES; OP=ADJ

L12 L11 and bit\$ and (error\$ or signal\$ or except\$)

35 L12

L11 L10 and switch\$

35 L11

L10 l3 and branch\$ and pipe\$

35 L10

DB=TDBD; PLUR=YES; OP=ADJ

L9 cpu and process and execut\$ and register and stack and virtual and overflow
and underflow program counter and variable\$ and accelerat\$

0 L9

DB=DWPI; PLUR=YES; OP=ADJ

L8 cpu and process and execut\$ and register and stack and virtual and overflow
and underflow program counter and variable\$ and accelerat\$

0 L8

DB=JPAB; PLUR=YES; OP=ADJ

Hit

Count

Set

Name

result set

<u>L7</u>	cpu and process and execut\$ and register and stack and virtual and overflow and underflow program counter and variable\$ and accelerat\$	0	<u>L7</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L6</u>	cpu and process and execut\$ and register and stack and virtual and overflow and underflow program counter and variable\$ and accelerat\$	0	<u>L6</u>
<i>DB=USOC; PLUR=YES; OP=ADJ</i>			
<u>L5</u>	cpu and process and execut\$ and register and stack and virtual and overflow and underflow program counter and variable\$ and accelerat\$	0	<u>L5</u>
<i>DB=PGPB; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	cpu and process and execut\$ and register and stack and virtual and overflow and underflow program counter and variable\$ and accelerat\$	0	<u>L4</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L3</u>	L2 and accelerat\$	36	<u>L3</u>
<u>L2</u>	L1 and program counter and variable\$	110	<u>L2</u>
<u>L1</u>	cpu and process and execut\$ and register and stack and virtual and overflow and underflow	236	<u>L1</u>

END OF SEARCH HISTORY



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Terms used

virtual and execute and cpu and overflow and underflow and operand and move and register and memory

1

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Best 200 shown

Relevance scale

1 [The KScalar simulator](#)

J. C. Moure, Dolores I. Rexachs, Emilio Luque

March 2002 **Journal on Educational Resources in Computing (JERIC)**, Volume 2 Issue 1

Full text available: [pdf\(493.35 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Modern processors increase their performance with complex microarchitectural mechanisms, which makes them more and more difficult to understand and evaluate. KScalar is a graphical simulation tool that facilitates the study of such processors. It allows students to analyze the performance behavior of a wide range of processor microarchitectures: from a very simple in-order, scalar pipeline, to a detailed out-of-order, superscalar pipeline with non-blocking caches, speculative execution, and comp ...

Keywords: Education, pipelined processor simulator

2 [The Clipper processor: instruction set architecture and implementation](#)

W. Hollingsworth, H. Sachs, A. J. Smith

February 1989 **Communications of the ACM**, Volume 32 Issue 2

Full text available: [pdf\(4.67 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Intergraph's CLIPPER microprocessor is a high performance, three chip module that implements a new instruction set architecture designed for convenient programmability, broad functionality, and easy future expansion.

3 [An example RISC vector machine architecture](#)

Martin Dowd

September 1987 **ACM SIGARCH Computer Architecture News**, Volume 15 Issue 4

Full text available: [pdf\(484.57 KB\)](#) Additional Information: [full citation](#), [index terms](#)

4 [A VLIW architecture for a trace scheduling compiler](#)

Robert P. Colwell, Robert P. Nix, John J. O'Donnell, David B. Papworth, Paul K. Rodman

October 1987 **Proceedings of the second international conference on Architectural support for programming languages and operating systems**, Volume 15 , 22 , 21 Issue 5 , 10 , 4

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Refine Search

Search Results -

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(712/244 712/210).ccls.	756

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IBM Technical Disclosure Bulletins

Search:

L4

Search History

DATE: Thursday, December 09, 2004 [Printable Copy](#) [Create Case](#)**Set Name Query**
side by side**Hit Count Set Name**
result set*DB=USPT; PLUR=YES; OP=ADJ*

<u>L4</u>	712/244,210.ccls.	756	<u>L4</u>
<u>L3</u>	717/147,148,149.ccls.	297	<u>L3</u>
<u>L2</u>	717/143.ccls.	168	<u>L2</u>
<u>L1</u>	717134,135,136,137,138,139,140.ccls.	0	<u>L1</u>

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